

Claims

- [c1] An analog/digital mixed integrated circuit that mixes analog circuits and digital circuits within the same semiconductor chip, comprising:
an analog circuit area and a digital circuit area within said semiconductor chip;
a clock generator circuit that generates a clock signal within said digital circuit area; and
a switching circuit that performs switching operation by said clock signal and performs analog signal processing within said digital circuit area.
- [c2] The analog/digital mixed integrated circuit according to claim 1, wherein said switching circuit is arranged near said clock generator circuit.
- [c3] An analog/digital mixed integrated circuit that mixes analog circuits and digital circuits within the same semiconductor chip, comprising:
a first switching circuit that operates based on a clock signal with a first frequency and performs analog signal processing;
a second switching circuit that operates based on a clock signal with a second frequency higher than said first fre-

quency and performs analog signal processing; and a clock generator circuit that generates a clock signal with said first and second frequency; wherein an analog circuit area and a digital circuit area are held within said semiconductor chip, said first switching circuit is arranged within said analog circuit area, and said second switching circuit and said clock generator circuit are arranged within said digital circuit area.

[c4] The analog/digital mixed integrated circuit according to claim 3, wherein said second switching circuit is arranged near said clock generator circuit.